

## LONBUILDER PROCESSOR BOARDS SUMMARY

MODEL 25000—LONBUILDER NEURON EMULATOR

MODEL 25200—LONBUILDER SINGLE BOARD COMPUTER (SBC)

MODEL 25400—LONBUILDER ROUTER

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### LONBUILDER PROCESSOR BOARDS SUMMARY

The LONBUILDER™ processor boards provide LONWORKS™ nodes for use during development.

#### The three LONBUILDER processor boards:

- ▼ *NEURON Emulator*      The starting point for NEURON® CHIP application development. Works with the NEURON C Debugger to provide a comprehensive debugging and prototyping environment.
- ▼ *SBC*      A prototype LONWORKS node for testing in a distributed environment.
- ▼ *Router*      Connects two LONWORKS communication channels.



Part Number 003-0003-01

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**Echelon Corporation**  
4015 Miranda Avenue  
Palo Alto, CA 94304  
Telephone (415) 855-7400  
Fax (415) 856-6153

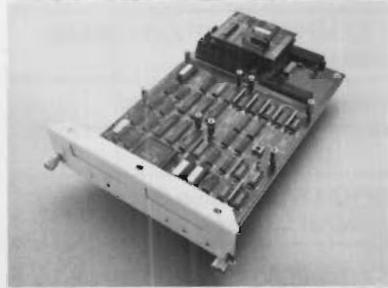
**Echelon Europe Ltd**  
105 Heath Street  
London NW3 6SS  
England  
Telephone (071) 431-1600  
Fax (071) 794-0532  
International Telephone + 44 71 431-1600  
International Fax + 44 71 794-0532

**Echelon Japan K.K.**  
AIDS Gotanda Building #808  
10-7, Higashi-Gotanda 1-chome,  
Shinagawa-ku, Tokyo 141, Japan  
Telephone (03) 3440-8638  
Fax (03) 3440-8639

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## LONBUILDER NEURON EMULATOR

### LONBUILDER NEURON EMULATOR



#### FEATURES AND BENEFITS

- ▼ *Configurable clock rate* Emulate any of the NEURON CHIP clock rates for accurate performance testing
- ▼ *64 KB of dual-ported RAM for code and data* Execute any application with no performance degradation
- ▼ *Hardware breakpoints by byte for up to 64 KB* The NEURON C Debugger can set a breakpoint on any statement without modifying application code
- ▼ *Read/write protect by byte for up to 64 KB* Emulate any NEURON 3150 or NEURON 3120™ memory map
- ▼ *Backplane transceiver* Inexpensive networking between processor boards installed in a development station
- ▼ *Accepts an optional transceiver expansion board* Simplified connection to other media using LONBUILDER transceivers, and rapid prototyping of custom transceivers
- ▼ *Accepts an optional I/O expansion board* Faster testing of custom I/O interfaces using the LONBUILDER I/O Evaluation Board, or using custom I/O expansion boards

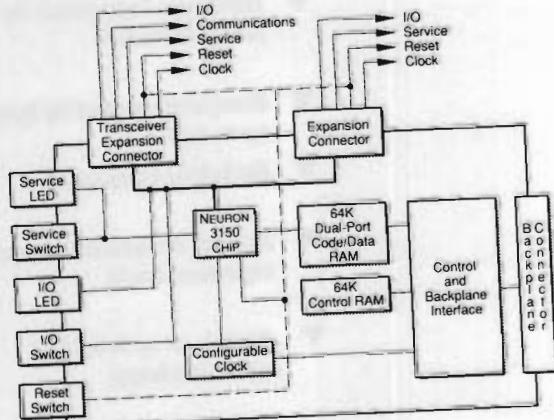
#### CONFIGURATION

The emulator must be installed in a development station, and reside on the same network as the LONBUILDER network management node. The emulator can be used with the development station backplane network for implementing a network consisting only of the boards installed within the development station. An optional transceiver must be added to each emulator for interfacing with other media and for interfacing with nodes outside the development station.

The emulator requires both the Development Station Software and the NEURON C Developer's Kit.

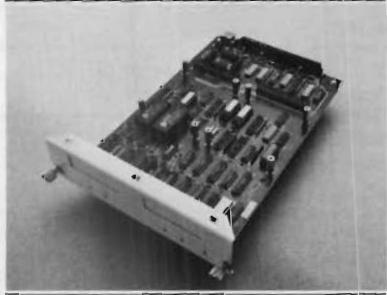
## TECHNICAL SPECIFICATIONS

Temperature	
Operating	0 to 40°C
Non-operating	-20 to 65°C
Operating Voltage	+5V @ 1.0A, plus expansion power (powered from development station)
Expansion Power (power supplied by the development station for use by expansion boards)	+5V @ 400mA per expansion board +/-12V @ 35mA per expansion board
Dimensions	168 mm x 267 mm x 19 mm (6.6" x 10.5" x 0.75")
Processor	NEURON 3150 CHIP
NEURON CHIP Input Clock Rate	10 MHz, 5 MHz, 2.5 MHz, 1.25 MHz, or 625 KHz (software configurable)
Network Interface	LONBUILDER transceiver expansion board. Backplane transceiver included; other transceivers optional
I/O Interface	IO.0 Display LED, IO.4 input button (jumper selectable), and optional I/O expansion board
Service Interface	Service button and service LED
Memory	64 KB Code/Data dual-ported RAM 64 KB Control RAM



## LONBUILDER SINGLE BOARD COMPUTER (SBC)

### LONBUILDER SINGLE BOARD COMPUTER (SBC)



#### FEATURES AND BENEFITS

- ▼ *Remote operation* Create true distributed networks with minimal hardware development
- ▼ *Configurable clock rate* Emulate any of the NEURON CHIP clock rates for accurate performance testing
- ▼ *64 KB of RAM for code and data* Execute any application with no performance degradation and download new application code over the network
- ▼ *Non-volatile RAM* Retains application and network image when power is cycled
- ▼ *Backplane transceiver* Inexpensive networking between processor boards installed in a development station
- ▼ *Accepts an optional transceiver expansion board* Simplified connection to other media using LONBUILDER transceivers and rapid prototyping of custom transceivers
- ▼ *Accepts an optional I/O expansion board* Faster testing of custom I/O interfaces using the LONBUILDER I/O Evaluation Board or using custom I/O expansion boards
- ▼ *Optional remote node enclosure available* Provides shielding and physical protection when used remote from a development station; required for use with an optional LONBUILDER RF transceiver

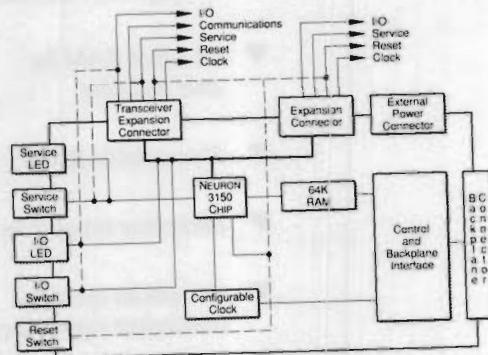
#### CONFIGURATION

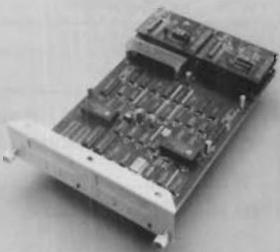
The SBC can be used with the development station backplane network for implementing a network consisting only of the boards installed within the development station. Optional transceivers must be added for each SBC for interfacing with other media and for interfacing with nodes outside the development station. Optional transceivers and +5V power must be added for each SBC located remote from a development station.

Development station hardware and software are required to load and configure an SBC.

## **TECHNICAL SPECIFICATIONS**

Temperature	
Operating	0 to 40°C
Non-operating	-20 to 65°C
Operating Voltage	+5V +/- 5% @ 1.0 A, plus expansion power (powered from development station or external power source)
Expansion Power (power supplied by the development station for use by expansion boards)	+5V @ 400mA per expansion board +/-12V @ 35mA per expansion board
Dimensions	168 mm x 267 mm x 19 mm (6.6" x 10.5" x 0.75")
Processor	NEURON 3150 CHIP
NEURON CHIP Input Clock Rate	10 MHz, 5 MHz, 2.5 MHz, 1.25 MHz, or 625 KHz (software and hardware configurable)
Network Interface	LONBUILDER transceiver expansion board. Backplane transceiver included; other transceivers optional
I/O Interface	IO.0 Display LED, IO.4 input button (jumper selectable), and optional I/O expansion board
Service Interface	Service button and service LED



**LONBUILDER  
ROUTER****LONBUILDER  
ROUTER****FEATURES AND BENEFITS**

- ▼ *Route between any pair of channels and media using 2 communication transceiver expansion boards* Supports multi-media networks, including combinations of RF, twisted pair, powerline, and custom media; also provides segmentation of networks for improved performance
- ▼ *Configurable clock rate (10 MHz and 5 MHz)* Emulate any of the NEURON CHIP bit rates for accurate performance testing
- ▼ *Priority handling of priority packets* Faster response time for priority messages
- ▼ *32 KB of RAM per channel* Provides buffering between two channels running at different bit rates
- ▼ *Backplane transceiver* Inexpensive routing between processor boards installed in a development station and other media with one additional transceiver
- ▼ *Accepts 2 optional transceiver expansion boards* Simplified connection to multiple media using LONBUILDER transceivers
- ▼ *Remote operation* Create true distributed networks with minimal hardware development
- ▼ *Optional remote node enclosure available* Provides shielding and physical protection when used remotely from a development station. Required for use with the optional LONBUILDER RF Transceiver

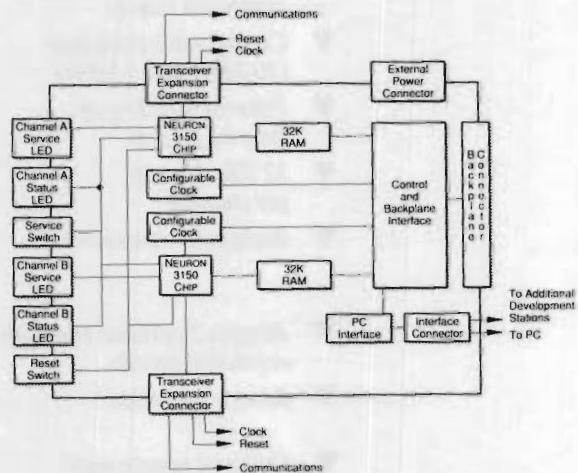
**CONFIGURATION**

The router can be used with the development station backplane network to route between the backplane network and external communication channels. When routing between the backplane and another channel, a single optional transceiver must be added for the second channel. The router may also be used for routing between two external channels, which requires two optional transceivers. The router may be used external to the development station, in which case two optional transceivers and external +5V power must be added.

Development station hardware and software are required to load and configure a router.

## TECHNICAL SPECIFICATIONS

Temperature	
Operating	0 to 40°C
Non-operating	-20 to 65°C
Operating Voltage	+5V +/- 5% @ 1.0A, plus expansion power (powered from development station or external power source)
Expansion Power (power supplied by the development station for use by expansion boards)	+5V @ 400mA per expansion board +/-12V @ 35mA per expansion board
Dimensions	168 mm x 267 mm x 19 mm (6.6" x 10.5" x 0.75")
Processors	2 NEURON 3150 CHIPS (one per channel)
NEURON CHIP Input Clock Rate	10 MHz or 5 MHz (software and hardware configurable)
Network Interfaces	LONBUILDER transceiver expansion board for each channel. One backplane transceiver included; other transceivers optional
Service Interface	Service LED for each NEURON 3150 CHIP and service button
Status Display	Network traffic indicator LED for each channel
Memory	32 KB non-volatile RAM per NEURON 3150 CHIP



### Literature Distribution Centers:

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